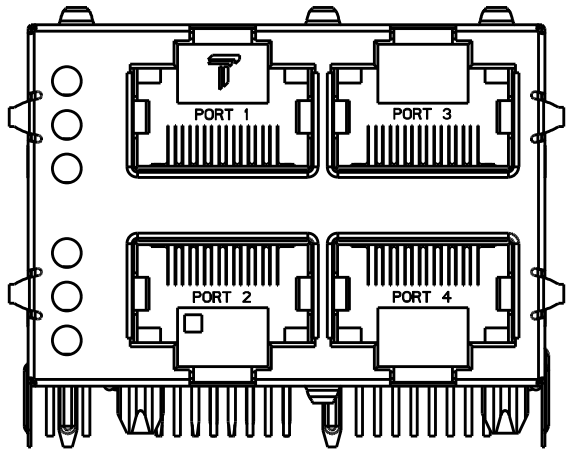


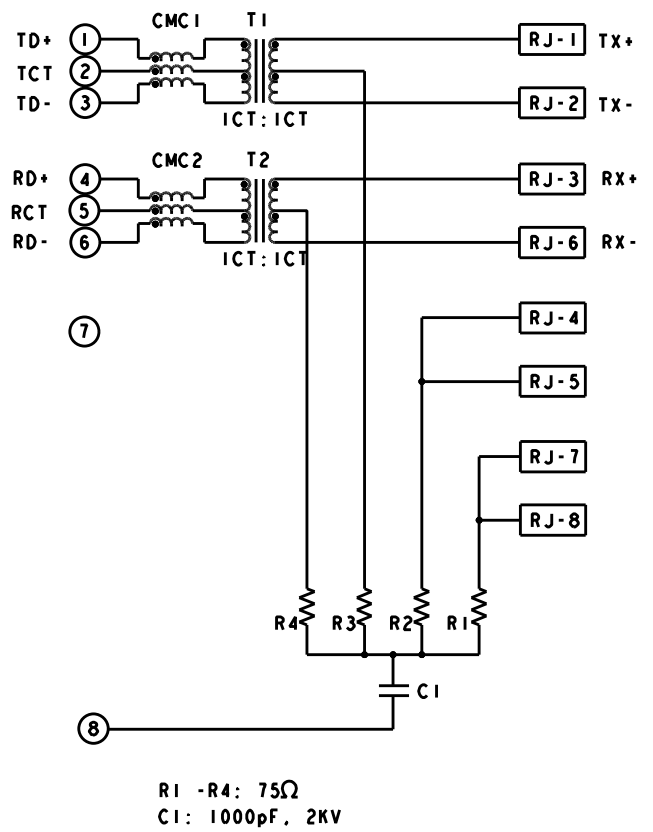
3D VIEW
SCALE 3:1



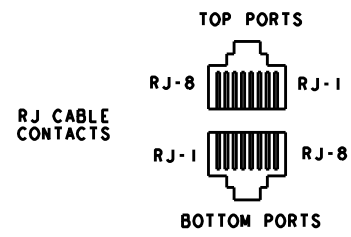
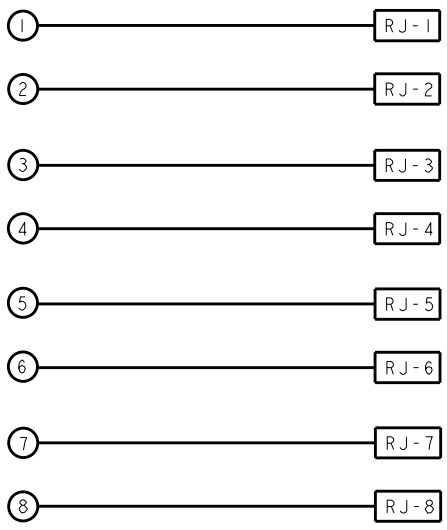
FRONT VIEW
SCALE 4:1

- △ MATERIALS:
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
 UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE
 AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
 ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
 UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH
 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
- △ MAGNETICS
 -APPLICATION: 10/100 BASE-T
 -IMPEDANCE: 100 OHMS
 -TURNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
 -OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
 8mADC BIAS FROM 0°C TO 70°C, TX AND RX
 -PERFORMANCE @ 25°C:
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
 18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
 12dB MIN FROM 60.1MHz TO 80MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 -ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF
 500V/SEC AND WITH ALL PORTS CONNECTED.
- △ TRP LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN,
 AGENCY APPROVAL MARKING IN APPROPRIATE LOCATION SHOWN.
 DATE CODE: YYWW WHERE YY IS YEAR, WW IS WORK WEEK,
 D IS DAY OF WEEK, WITH SUNDAY=1
- 4. OPERATING TEMP: FROM 0°C TO +70°C.
- △ RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
- △ LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
 LED COLOR: DOMINANT WAVELENGTH (D): GREEN 568 nm TYP. @ IF=20mA
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
- △ DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
- 8. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS,
 PEAK TEMPERATURE 260°C MAX, 10sec MAX.
- 9. THIS COMPONENT IS UL LISTED.

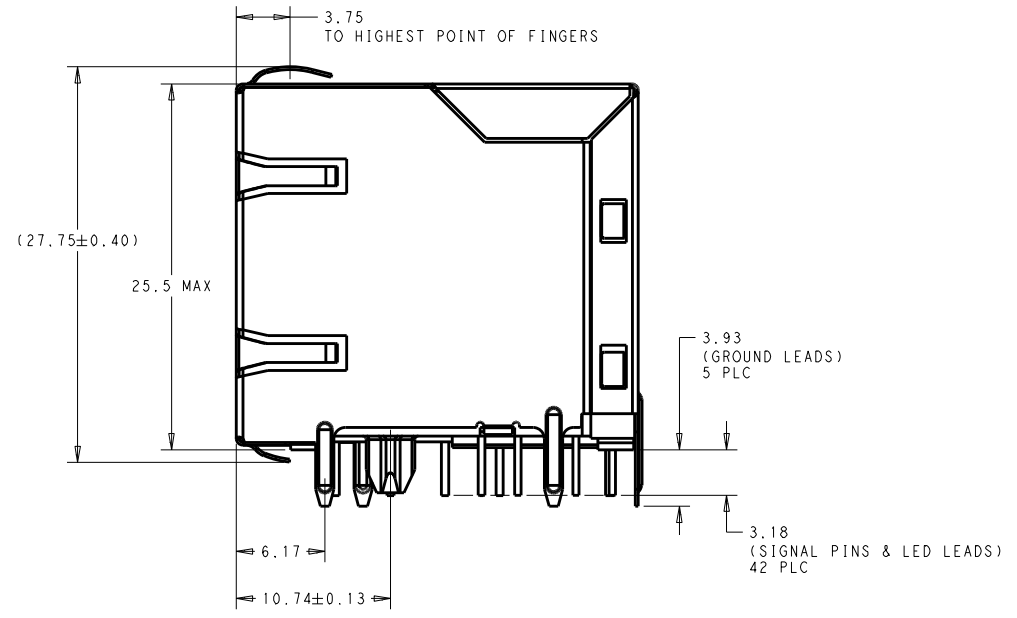
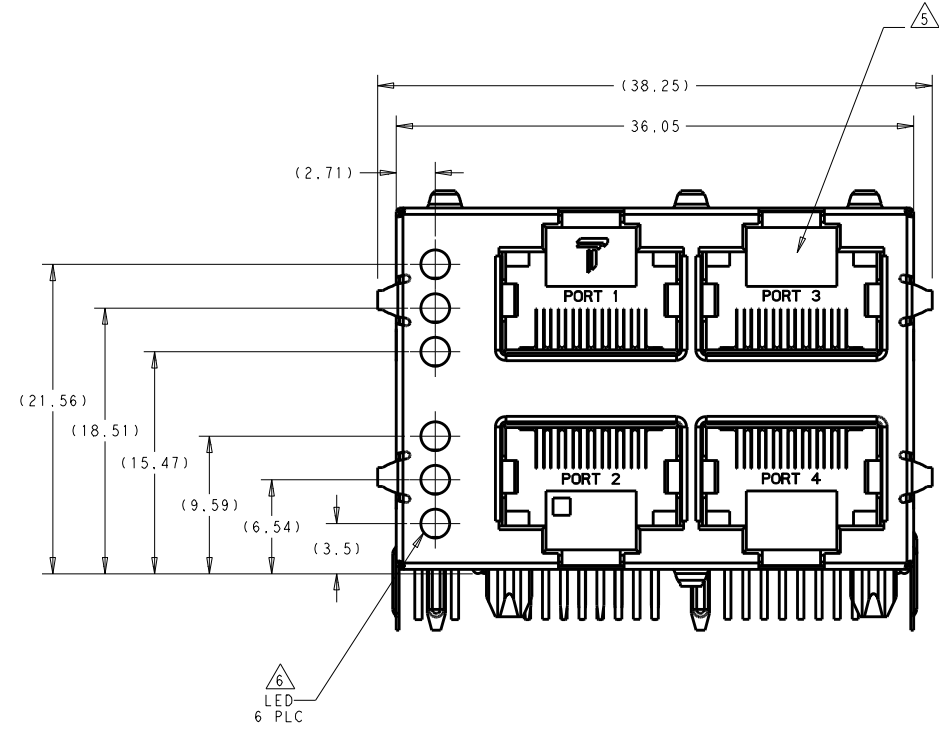
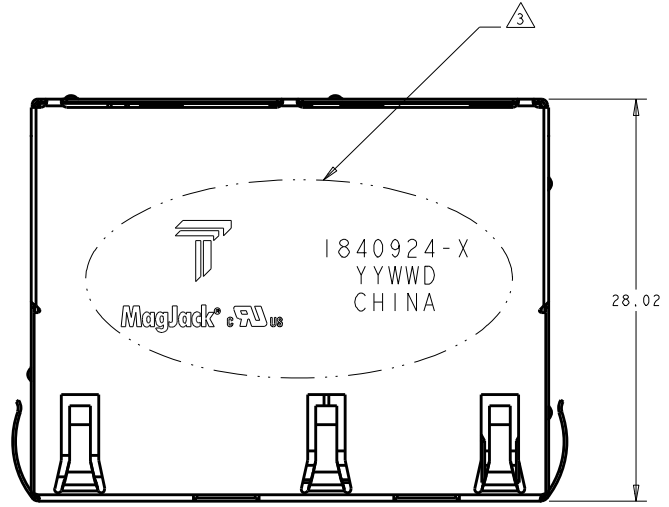
S743PI 10/100 BASE-T CIRCUIT △
FOR PORT 1 AND PORT 2



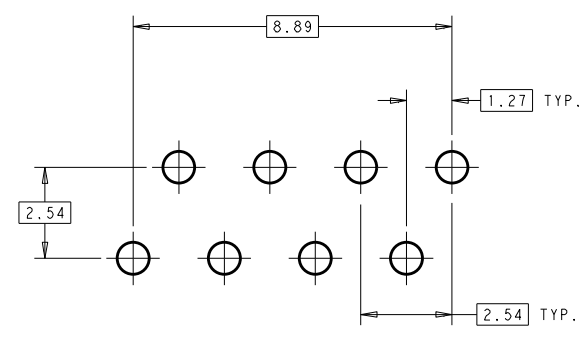
THROUGHT CIRCUIT
FOR PORT 3 AND PORT 4



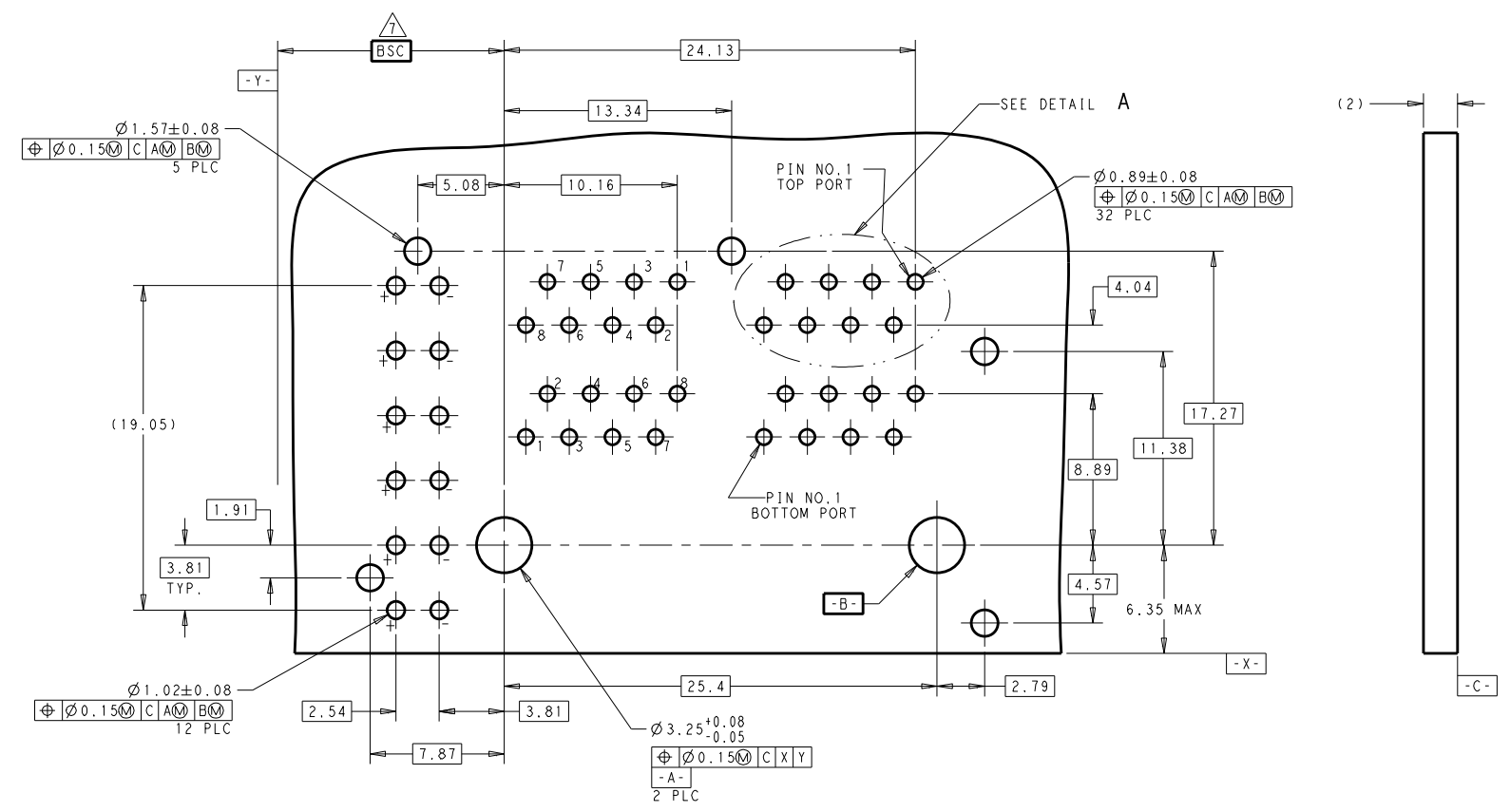
THIS DRAWING IS A CONTROLLED DOCUMENT.		DRN CLOUD ZENG 06JAN2010	DRN	GREEN	1840924-1
DIMENSIONS: mm		CHK TONY YUAN 06JAN2010	DRN	SIDE LED (6 PLC)	PART NUMBER
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD KELETH ZHU 06JAN2010	DRN	trp CONNECTOR DONGGUAN CHINA	
0 PLC ±		MODEL NAME	DESC	2X2 S743PI 10/100 W/ LED	
1 PLC ±		MAGJACK	SIZE	A1	
2 PLC ±0.25		STACK NON-POE	CAGE CODE	DRAWING NO	
3 PLC ±		108-104004	SCALE	NTS	
4 PLC ±		108-104004	SHEET	1 OF 3	
ANGLES		108-104004	REV	F	



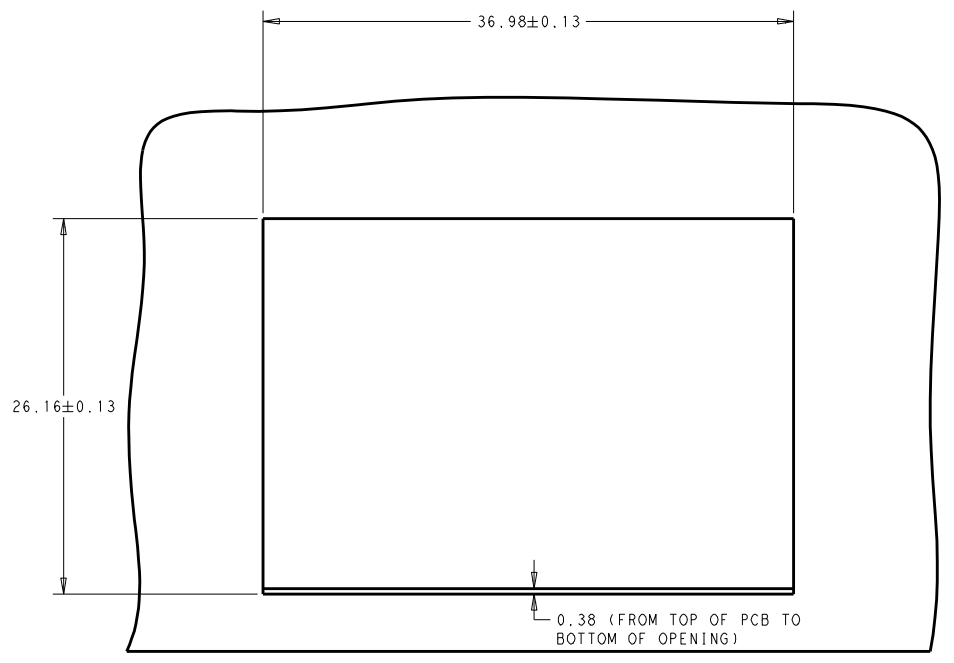
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: CLOUD ZENG 06 JAN 2010 CRK: TONY YUAN 06 JAN 2010 APVD: KE LTH ZHU 06 JAN 2010	trp CONNECTOR DONGGUAN CHINA
DIMENSIONS: mm	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ±0.25 3 PLC ± 4 PLC ±	MODEL NAME: MAGJACK DESC: 2X2 S743P1 10/100 W/ LED	
PRODUCT SPEC: 108-104004	APPLICATION SPEC:	SIZE: A1 CAGE CODE: 1840924 DRAWING NO: 1840924	RESTRICTED TO:
CUSTOMER DRAWING		SCALE: NTS	SHEET 2 OF 3 REV F



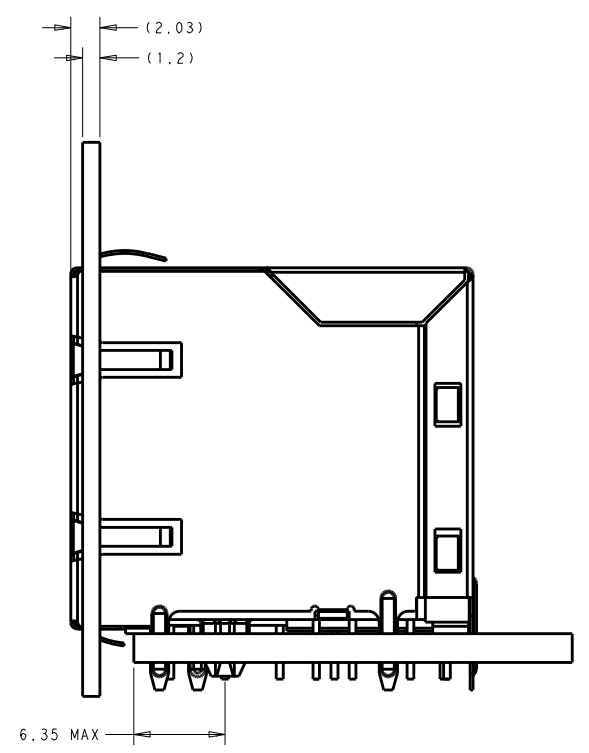
TYPICAL HOLE LAYOUT
DETAIL A
SCALE 10:1
(4 POSITIONS)



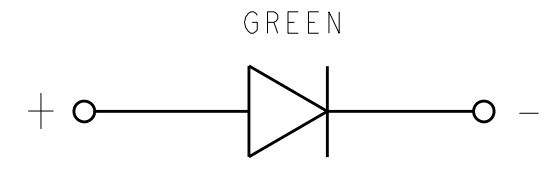
RECOMMENDED PC BOARD LAYOUT
(VIEW FROM COMPONENT SIDE)
SCALE 5:1



SUGGESTED PANEL OPENING



SIDE VIEW SHOWN WITH PANEL
AND CUSTOMER PCB
FOR LOCATION



LED CONFIGURATION
(6 POSITIONS)

THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 06 JAN 2010	DONGGUAN CHINA
DIMENSIONS: mm		DWG: CLOUD ZENG CHK: TONY YUAN APVD: KE LTH ZHU	
0 PLC ± 1 PLC ± 2 PLC ±0.25 3 PLC ± 4 PLC ±		MODEL NAME: MAGJACK STACK NON-POE	DESC: 2X2 S743P1 10/100 W/ LED
PRODUCT SPEC: 108-104004	APPLICATION SPEC: -	SIZE: A1 CAGE CODE: - DRAWING NO: 1840924	RESTRICTED TO: - SCALE: NTS SHEET: 3 OF 3 REV: F