# MC74LCX257

## Low-Voltage CMOS Quad 2-Input Multiplexer

### With 5.0 V−Tolerant Inputs and Outputs (3−State, Non−Inverting)

The MC74LCX257 is a high performance, quad 2−input multiplexer with 3−state outputs operating from a 2.3 to 3.6 V supply. High impedance TTL compatible inputs significantly reduce current loading to input drivers while TTL compatible outputs offer improved switching noise performance. A $V_I$ specification of 5.5 V allows MC74LCX257 inputs to be safely driven from 5.0 V devices.

Four bits of data from two sources can be selected using the Select input. The four outputs present the selected data in the true (non−inverted) form. The outputs may be switched to a high impedance state by placing a logic HIGH on the Output Enable ($\overline{OE}$) input. Current drive capability is 24 mA at the outputs.
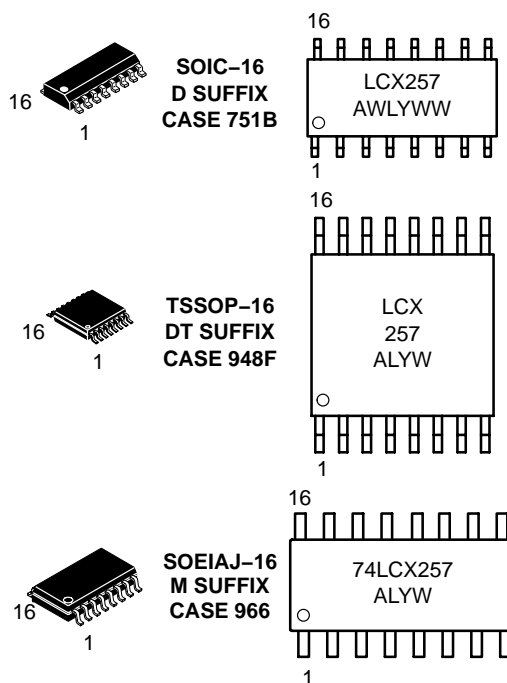
### Features

- Designed for 2.3 to 3.6 V $V_{CC}$ Operation
- 5.0 V Tolerant − Interface Capability with 5.0 V TTL Logic
- Supports Live Insertion and Withdrawal
- $I_{OFF}$ Specification Guarantees High Impedance When $V_{CC} = 0$ V
- LVTTL Compatible
- LVCMOS Compatible
- 24 mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current in All Three Logic States (10 μA) Substantially Reduces System Power Requirements
- Latchup Performance Exceeds 500 mA
- ESD Performance:    Human Body Model >2000 V
  Machine Model >200 V
- Pb−Free Packages are Available*

## ON Semiconductor®

http://onsemi.com

### MARKING DIAGRAMS

SOIC−16
D SUFFIX
CASE 751B

16

LCX257
AWLYWW

1

TSSOP−16
DT SUFFIX
CASE 948F

16

LCX
257
ALYW

1

SOEIAJ−16
M SUFFIX
CASE 966

16

74LCX257
ALYW

1

A = Assembly Location
L, WL = Wafer Lot
Y = Year
W, WW = Work Week

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.
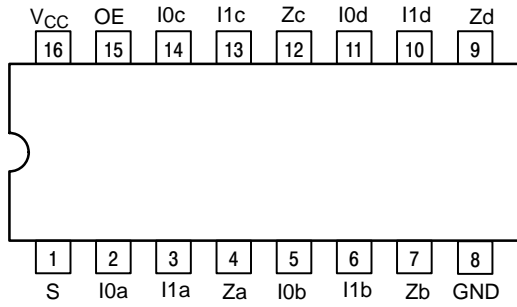
# MC74LCX257

| VCC | OE | I0c | I1c | Zc | I0d | I1d | Zd |
|---|---|---|---|---|---|---|---|
| 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 |

| 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
|---|---|---|---|---|---|---|---|
| S | I0a | I1a | Za | I0b | I1b | Zb | GND |

**Figure 1. Pinout: 16−Lead Plastic Package**
(Top View)
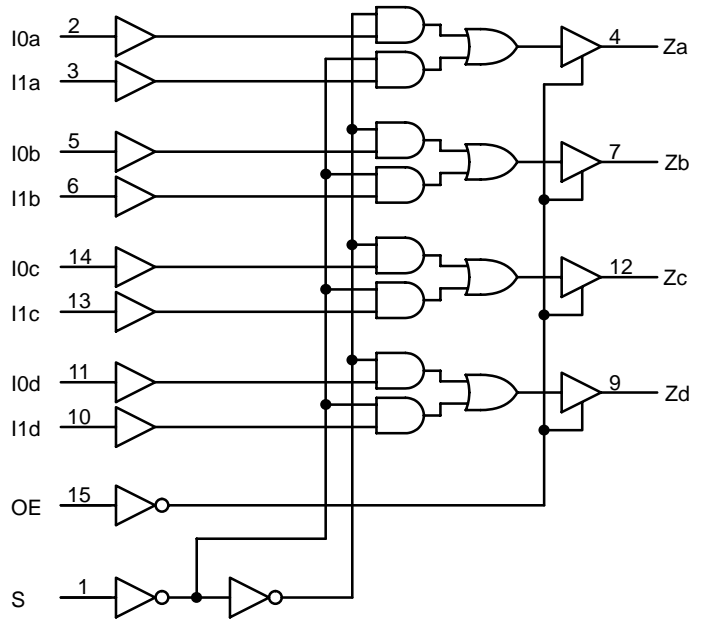
**Figure 2. Logic Diagram**

**PIN NAMES**

| Pins | Function |
|---|---|
| I0n | Source 0 Data Inputs |
| I1n | Source 1 Data Inputs |
| $\overline{OE}$ | Output Enable Input |
| S | Select Input |
| Zn | Outputs |

**TRUTH TABLE**

| Inputs | | | | Outputs |
|---|---|---|---|---|
| $\overline{OE}$ | S | I0n | I1n | Zn |
| H | X | X | X | Z |
| L | H | X | L | L |
| L | H | X | H | H |
| L | L | L | X | L |
| L | L | H | X | H |

H = High Voltage Level
L = Low Voltage Level
X = High or Low Voltage Level and Transitions are Acceptable
Z = High Impedance State

For ICC reasons, DO NOT FLOAT Inputs

# MC74LCX257

## MAXIMUM RATINGS

| Symbol | Parameter | Value | Condition | Unit |
|--------|-----------|-------|-----------|------|
| $V_{CC}$ | DC Supply Voltage | −0.5 to +7.0 | | V |
| $V_I$ | DC Input Voltage | $−0.5 \leq V_I \leq +7.0$ | | V |
| $V_O$ | DC Output Voltage | $−0.5 \leq V_I \leq +7.0$ | Output in 3−State | V |
| | | $−0.5 \leq V_O \leq V_{CC} + 0.5$ | Output in HIGH or LOW State (Note 1) | V |
| $I_{IK}$ | DC Input Diode Current | −50 | $V_I < GND$ | mA |
| $I_{OK}$ | DC Output Diode Current | −50 | $V_O < GND$ | mA |
| | | +50 | $V_O > V_{CC}$ | mA |
| $I_O$ | DC Output Source/Sink Current | ±50 | | mA |
| $I_{CC}$ | DC Supply Current Per Supply Pin | ±100 | | mA |
| $I_{GND}$ | DC Ground Current Per Ground Pin | ±100 | | mA |
| $T_{STG}$ | Storage Temperature Range | −65 to +150 | | °C |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.
1. $I_O$ absolute maximum rating must be observed.

## RECOMMENDED OPERATING CONDITIONS

| Symbol | Parameter | | Min | Type | Max | Unit |
|--------|-----------|--|-----|------|-----|------|
| $V_{CC}$ | Supply Voltage | Operating<br>Data Retention Only | 2.0<br>1.5 | 2.5, 3.3<br>2.5, 3.3 | 3.6<br>3.6 | V |
| $V_I$ | Input Voltage | | 0 | | 5.5 | V |
| $V_O$ | Output Voltage | (HIGH or LOW State)<br>(3−State) | 0<br>0 | | $V_{CC}$<br>5.5 | V |
| $I_{OH}$ | HIGH Level Output Current | $V_{CC} = 3.0\ V – 3.6\ V$<br>$V_{CC} = 2.7\ V – 3.0\ V$<br>$V_{CC} = 2.3\ V – 2.7\ V$ | | | −24<br>−12<br>−8 | mA |
| $I_{OL}$ | LOW Level Output Current | $V_{CC} = 3.0\ V – 3.6\ V$<br>$V_{CC} = 2.7\ V – 3.0\ V$<br>$V_{CC} = 2.3\ V – 2.7\ V$ | | | +24<br>+12<br>+8 | mA |
| $T_A$ | Operating Free−Air Temperature | | −40 | | +85 | °C |
| $\Delta t/\Delta V$ | Input Transition Rise or Fall Rate, $V_{IN}$ from 0.8 V to 2.0 V, $V_{CC}$ = 3.0 V | | 0 | | 10 | ns/V |

## ORDERING INFORMATION

| Device | Package | Shipping[†] |
|--------|---------|-------------|
| MC74LCX257DR2 | SOIC−16 | 2500 Tape & Reel |
| MC74LCX257DR2G | SOIC−16<br>(Pb−Free) | 2500 Tape & Reel |
| MC74LCX257DT | TSSOP−16* | 96 Units / Rail |
| MC74LCX257DTR2 | TSSOP−16* | 2500 Tape & Reel |
| MC74LCX257M | SOEIAJ−16 | 48 Units / Rail |
| MC74LCX257MEL | SOEIAJ−16 | 2000 Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
*This package is inherently Pb−Free.

## DC ELECTRICAL CHARACTERISTICS

| Symbol | Characteristic | Condition | $T_A = -40°C$ to $+85°C$ Min | Max | Unit |
|---|---|---|---|---|---|
| $V_{IH}$ | HIGH Level Input Voltage (Note 2) | $2.3\ V \le V_{CC} \le 2.7\ V$ | 1.7 | | V |
| | | $2.7\ V \le V_{CC} \le 3.6\ V$ | 2.0 | | |
| $V_{IL}$ | LOW Level Input Voltage (Note 2) | $2.3\ V \le V_{CC} \le 2.7\ V$ | | 0.7 | V |
| | | $2.7\ V \le V_{CC} \le 3.6\ V$ | | 0.8 | |
| $V_{OH}$ | HIGH Level Output Voltage | $2.3\ V \le V_{CC} \le 3.6\ V;\ I_{OH} = -100\ \mu A$ | $V_{CC} - 0.2$ | | V |
| | | $V_{CC} = 2.3\ V;\ I_{OH} = -8\ mA$ | 1.8 | | |
| | | $V_{CC} = 2.7\ V;\ I_{OH} = -12\ mA$ | 2.2 | | |
| | | $V_{CC} = 3.0\ V;\ I_{OH} = -18\ mA$ | 2.4 | | |
| | | $V_{CC} = 3.0\ V;\ I_{OH} = -24\ mA$ | 2.2 | | |
| $V_{OL}$ | LOW Level Output Voltage | $2.3\ V \le V_{CC} \le 3.6\ V;\ I_{OL} = 100\ \mu A$ | | 0.2 | V |
| | | $V_{CC} = 2.3\ V;\ I_{OL} = 8\ mA$ | | 0.6 | |
| | | $V_{CC} = 2.7\ V;\ I_{OL} = 12\ mA$ | | 0.4 | |
| | | $V_{CC} = 3.0\ V;\ I_{OL} = 16\ mA$ | | 0.4 | |
| | | $V_{CC} = 3.0\ V;\ I_{OL} = 24\ mA$ | | 0.55 | |
| $I_I$ | Input Leakage Current | $2.3\ V \le V_{CC} \le 3.6\ V;\ 0\ V \le V_I \le 5.5\ V$ | | $\pm 5$ | $\mu A$ |
| $I_{OZ}$ | 3–State Output Current | $2.3 \le V_{CC} \le 3.6\ V;\ 0\ V \le V_O \le 5.5\ V;$ $V_I = V_{IH}$ or $V_{IL}$ | | $\pm 5$ | $\mu A$ |
| $I_{OFF}$ | Power–Off Leakage Current | $V_{CC} = 0\ V;\ V_I$ or $V_O = 5.5\ V$ | | 10 | $\mu A$ |
| $I_{CC}$ | Quiescent Supply Current | $2.3 \le V_{CC} \le 3.6\ V;\ V_I = GND$ or $V_{CC}$ | | 10 | $\mu A$ |
| | | $2.3 \le V_{CC} \le 3.6\ V;\ 3.6 \le V_I$ or $V_O \le 5.5\ V$ | | $\pm 10$ | |
| $\Delta I_{CC}$ | Increase in $I_{CC}$ per Input | $2.3 \le V_{CC} \le 3.6\ V;\ V_{IH} = V_{CC} - 0.6\ V$ | | 500 | $\mu A$ |

2. These values of $V_I$ are used to test DC electrical characteristics only.

## AC CHARACTERISTICS $t_R = t_F = 2.5$ ns; $R_L = 500\ \Omega$

| Symbol | Parameter | Waveform | Limits $T_A = -40°C$ to $+85°C$ $V_{CC} = 3.3\ V \pm 0.3\ V$ $C_L = 50\ pF$ Min | Max | $V_{CC} = 2.7\ V$ $C_L = 50\ pF$ Min | Max | $V_{CC} = 2.5\ V \pm 0.2\ V$ $C_L = 30\ pF$ Min | Max | Unit |
|---|---|---|---|---|---|---|---|---|---|
| $t_{PLH}$ | Propagation Delay | 1 | 1.5 | 6.0 | 1.5 | 6.5 | 1.5 | 7.2 | ns |
| $t_{PHL}$ | In to Zn | | 1.5 | 6.0 | 1.5 | 6.5 | 1.5 | 7.2 | |
| $t_{PLH}$ | Propagation Delay | 1, 2 | 1.5 | 7.0 | 1.5 | 8.5 | 1.5 | 9.1 | ns |
| $t_{PHL}$ | S to Zn | | 1.5 | 7.0 | 1.5 | 8.5 | 1.5 | 9.1 | |
| $t_{PZH}$ | Output Enable Time to | 3 | 1.5 | 7.0 | 1.5 | 8.5 | 1.5 | 9.1 | ns |
| $t_{PZL}$ | High and Low Level | | 1.5 | 7.0 | 1.5 | 8.5 | 1.5 | 9.1 | |
| $t_{PHZ}$ | Output Disable Time From | 3 | 1.5 | 5.5 | 1.5 | 6.0 | 1.5 | 6.6 | ns |
| $t_{PLZ}$ | High and Low Level | | 1.5 | 5.5 | 1.5 | 6.0 | 1.5 | 6.6 | |
| $t_{OSHL}$ | Output–to–Output Skew | | | 1.0 | | | | | ns |
| $t_{OSLH}$ | (Note 3) | | | 1.0 | | | | | |

3. Skew is defined as the absolute value of the difference between the actual propagation delay for any two separate outputs of the same device. The specification applies to any outputs switching in the same direction, either HIGH–to–LOW ($t_{OSHL}$) or LOW–to–HIGH ($t_{OSLH}$); parameter guaranteed by design.

## DYNAMIC SWITCHING CHARACTERISTICS

| Symbol | Characteristic | Condition | $T_A$ = +25°C | | | Unit |
|---|---|---|---|---|---|---|
| | | | Min | Typ | Max | |
| $V_{OLP}$ | Dynamic LOW Peak Voltage (Note 4) | $V_{CC}$ = 3.3 V, $C_L$ = 50 pF, $V_{IH}$ = 3.3 V, $V_{IL}$ = 0 V | | 0.8 | | V |
| | | $V_{CC}$ = 2.5 V, $C_L$ = 30 pF, $V_{IH}$ = 2.5 V, $V_{IL}$ = 0 V | | 0.6 | | V |
| $V_{OLV}$ | Dynamic LOW Valley Voltage (Note 4) | $V_{CC}$ = 3.3 V, $C_L$ = 50 pF, $V_{IH}$ = 3.3 V, $V_{IL}$ = 0 V | | −0.8 | | V |
| | | $V_{CC}$ = 2.5 V, $C_L$ = 30 pF, $V_{IH}$ = 2.5 V, $V_{IL}$ = 0 V | | −0.6 | | V |

4. Number of outputs defined as "n". Measured with "n−1" outputs switching from HIGH−to−LOW or LOW−to−HIGH. The remaining output is measured in the LOW state.

## CAPACITIVE CHARACTERISTICS

| Symbol | Parameter | Condition | Typical | Unit |
|---|---|---|---|---|
| $C_{IN}$ | Input Capacitance | $V_{CC}$ = 3.3 V, $V_I$ = 0 V or $V_{CC}$ | 7 | pF |
| $C_{I/O}$ | Input/Output Capacitance | $V_{CC}$ = 3.3 V, $V_I$ = 0 V or $V_{CC}$ | 8 | pF |
| $C_{PD}$ | Power Dissipation Capacitance | 10 MHz, $V_{CC}$ = 3.3 V, $V_I$ = 0 V or $V_{CC}$ | 25 | pF |
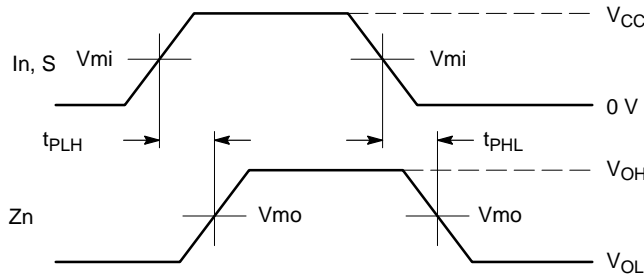


**WAVEFORM 1 – NON–INVERTING PROPAGATION DELAYS**
$t_R$ = $t_F$ = 2.5 ns, 10% to 90%; f = 1.0 MHz; $t_W$ = 500 ns
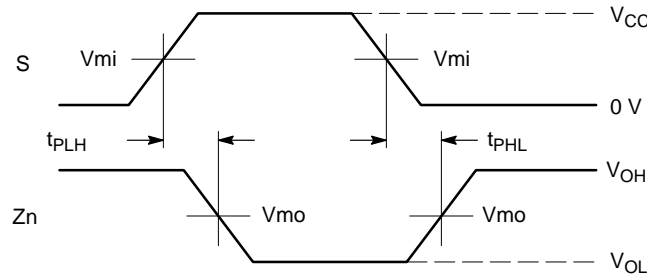


**WAVEFORM 2 – INVERTING PROPAGATION DELAYS**
$t_R$ = $t_F$ = 2.5 ns, 10% to 90%; f = 1.0 MHz; $t_W$ = 500 ns
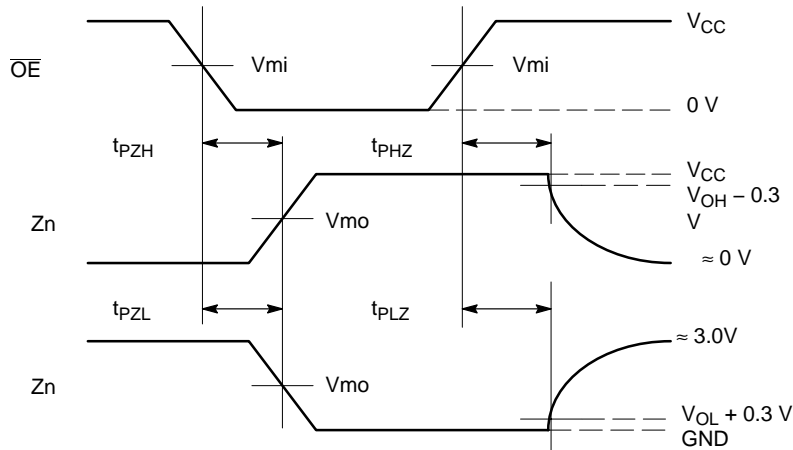


**WAVEFORM 3 – OUTPUT ENABLE AND DISABLE TIMES**
$t_R$ = $t_F$ = 2.5 ns, 10% to 90%; f = 1.0 MHz; $t_W$ = 500 ns

| Symbol | Vcc | | |
|---|---|---|---|
| | 3.3 V ± 0.3 V | 2.7 V | 2.5 V ± 0.2 V |
| Vmi | 1.5 V | 1.5 V | Vcc/2 |
| Vmo | 1.5 V | 1.5 V | Vcc/2 |
| $V_{HZ}$ | $V_{OL}$ + 0.3 V | $V_{OL}$ + 0.3 V | $V_{OL}$ + 0.15 V |
| $V_{LZ}$ | $V_{OH}$ − 0.3 V | $V_{OH}$ − 0.3 V | $V_{OH}$ − 0.15 V |

**Figure 3. AC Waveforms**

# MC74LCX257

## PACKAGE DIMENSIONS

### SOIC–16
### D SUFFIX
CASE 751B–05
ISSUE J
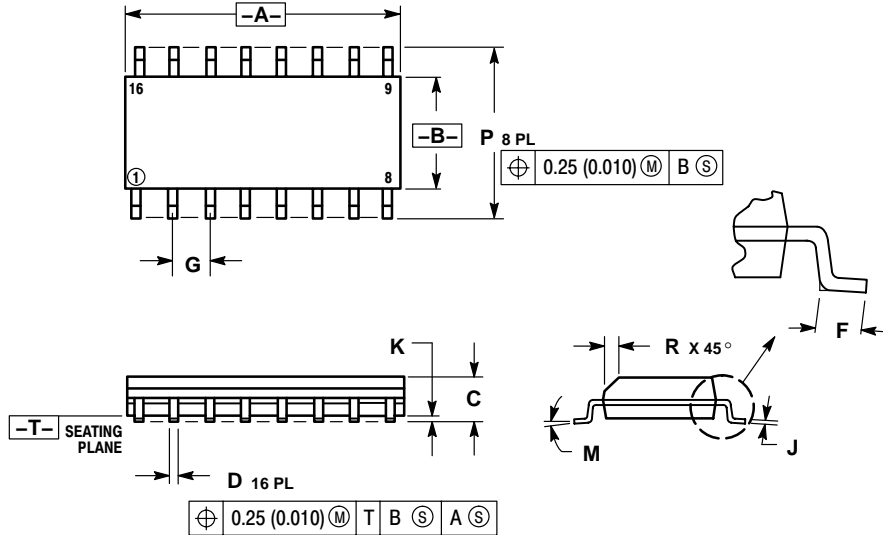
**P 8 PL**

$\oplus$ | 0.25 (0.010) Ⓜ | B Ⓢ

K

C

**−T−** SEATING PLANE

**D 16 PL**

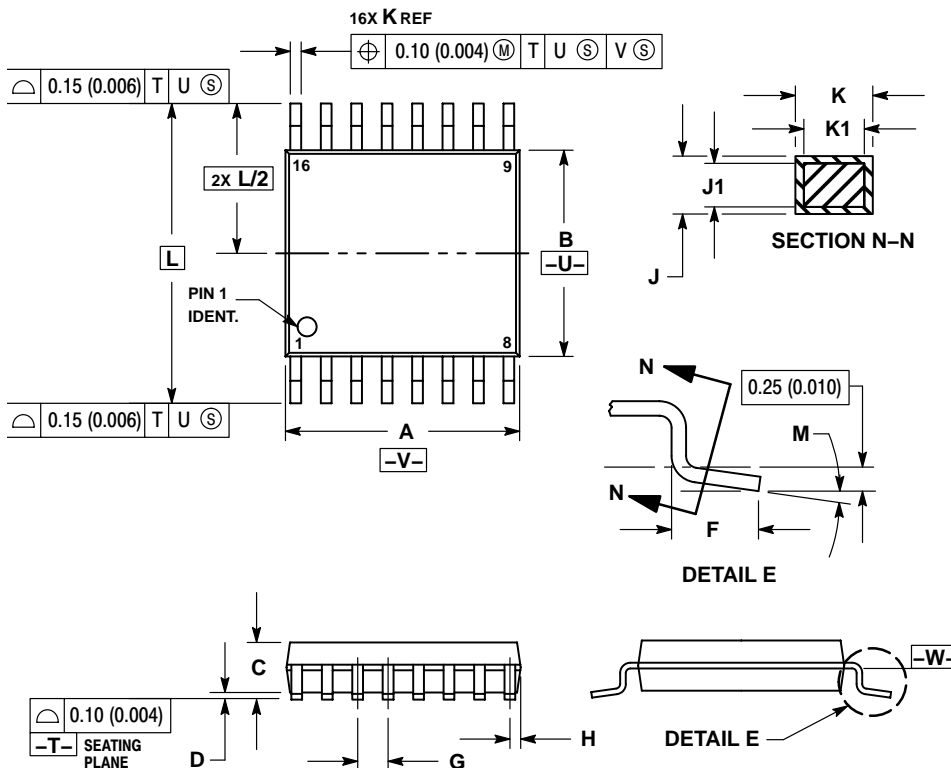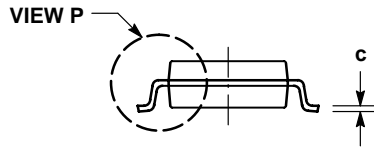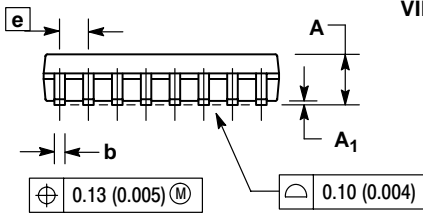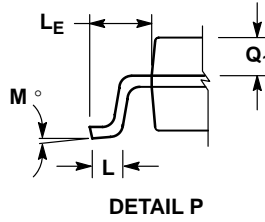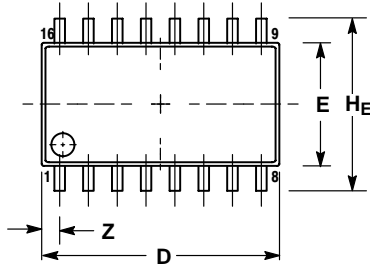$\oplus$ | 0.25 (0.010) Ⓜ | T | B Ⓢ | A Ⓢ

R X 45°

M

J

F

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

|  | MILLIMETERS | | INCHES | |
|---|---|---|---|---|
| DIM | MIN | MAX | MIN | MAX |
| A | 9.80 | 10.00 | 0.386 | 0.393 |
| B | 3.80 | 4.00 | 0.150 | 0.157 |
| C | 1.35 | 1.75 | 0.054 | 0.068 |
| D | 0.35 | 0.49 | 0.014 | 0.019 |
| F | 0.40 | 1.25 | 0.016 | 0.049 |
| G | 1.27 BSC | | 0.050 BSC | |
| J | 0.19 | 0.25 | 0.008 | 0.009 |
| K | 0.10 | 0.25 | 0.004 | 0.009 |
| M | 0° | 7° | 0° | 7° |
| P | 5.80 | 6.20 | 0.229 | 0.244 |
| R | 0.25 | 0.50 | 0.010 | 0.019 |

### TSSOP–16
### DT SUFFIX
CASE 948F–01
ISSUE O

**16X K REF**

$\oplus$ | 0.10 (0.004) Ⓜ | T | U Ⓢ | V Ⓢ

▱ | 0.15 (0.006) | T | U Ⓢ

**2X L/2**

L

PIN 1 IDENT.

▱ | 0.15 (0.006) | T | U Ⓢ

**B**
**−U−**

**A**
**−V−**

K

K1

J1

J

**SECTION N−N**

N

N

0.25 (0.010)

M

F

**DETAIL E**

C

▱ | 0.10 (0.004)

**−T−** SEATING PLANE

D

G

H

**DETAIL E**

**−W−**

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH. PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE −W−.

|  | MILLIMETERS | | INCHES | |
|---|---|---|---|---|
| DIM | MIN | MAX | MIN | MAX |
| A | 4.90 | 5.10 | 0.193 | 0.200 |
| B | 4.30 | 4.50 | 0.169 | 0.177 |
| C | −−− | 1.20 | −−− | 0.047 |
| D | 0.05 | 0.15 | 0.002 | 0.006 |
| F | 0.50 | 0.75 | 0.020 | 0.030 |
| G | 0.65 BSC | | 0.026 BSC | |
| H | 0.18 | 0.28 | 0.007 | 0.011 |
| J | 0.09 | 0.20 | 0.004 | 0.008 |
| J1 | 0.09 | 0.16 | 0.004 | 0.006 |
| K | 0.19 | 0.30 | 0.007 | 0.012 |
| K1 | 0.19 | 0.25 | 0.007 | 0.010 |
| L | 6.40 BSC | | 0.252 BSC | |
| M | 0° | 8° | 0° | 8° |

# MC74LCX257

## PACKAGE DIMENSIONS

**SOEIAJ–16**
**M SUFFIX**
CASE 966–01
ISSUE O

**DETAIL P**

**VIEW P**

| $\oplus$ | 0.13 (0.005) Ⓜ |

| △ | 0.10 (0.004) |

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS AND ARE MEASURED AT THE PARTING LINE. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
5. THE LEAD WIDTH DIMENSION (b) DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OR THE FOOT. MINIMUM SPACE BETWEEN PROTRUSIONS AND ADJACENT LEAD TO BE 0.46 ( 0.018).

| DIM | MILLIMETERS | | INCHES | |
|---|---|---|---|---|
| | MIN | MAX | MIN | MAX |
| A | --- | 2.05 | --- | 0.081 |
| $A_1$ | 0.05 | 0.20 | 0.002 | 0.008 |
| b | 0.35 | 0.50 | 0.014 | 0.020 |
| c | 0.18 | 0.27 | 0.007 | 0.011 |
| D | 9.90 | 10.50 | 0.390 | 0.413 |
| E | 5.10 | 5.45 | 0.201 | 0.215 |
| e | 1.27 BSC | | 0.050 BSC | |
| $H_E$ | 7.40 | 8.20 | 0.291 | 0.323 |
| L | 0.50 | 0.85 | 0.020 | 0.033 |
| $L_E$ | 1.10 | 1.50 | 0.043 | 0.059 |
| M | 0 ° | 10 ° | 0 ° | 10 ° |
| $Q_1$ | 0.70 | 0.90 | 0.028 | 0.035 |
| Z | --- | 0.78 | --- | 0.031 |