



**MICROCHIP**

**rfPIC12F675K/675F/675H**

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**rfPIC12F675K/675F/675H Rev. A Silicon/Data Sheet Errata**

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The rfPIC12F675K/675F/675H parts you have received conform functionally to the Device Data Sheet (DS70091A), except for the anomalies described below.

All the problems listed here will be addressed in future revisions of the rfPIC12F675K/675F/675H silicon.

**1. Module: VDD and VDDRF Supply Voltage**

VDD and VDDRF Supply Voltage maximum has been reduced to 4.5V.

**Work around**

Reduce VDD and VDDRF Supply Voltage to less than or equal to 4.5V.

**Date Codes that pertain to this issue:**

All engineering and production devices beginning with date code 0340XXX.

**2. Module: Loop Filter (LF) Pin 13 changed to no connection**

The loop filter (LF) signal will no longer be bonded out. Pin 13 is a no connection.

**Work around**

Remove all circuitry to the LF pin.

**Date Codes that pertain to this issue:**

All engineering and production devices beginning with date code 0340XXX.

**3. Module: Reference Clock (REFCLK) Pin 7 changed to no connection**

The reference clock (REFCLK) signal will no longer be bonded out. Pin 7 is a no connection.

**Work around**

Remove all circuitry to the REFCLK pin.

**Date Codes that pertain to this issue:**

All engineering and production devices beginning with date code 0340XXX.

**Clarifications/Corrections to the Data Sheet:**

In the Device Data Sheet (DS70091A), the following clarifications and corrections should be noted.

**1. Module: Loop Filter (LF) Pin 13 changed to no connection**

LF pin 13 has been changed to no connection (NC). All references in the data sheet referring to the LF pin should be deleted.

- Pin Diagram page 1: replace LF with NC (no connection).
- Figure 1-1: remove LF pin connection.
- Table 1-1: replace LF with NC (no connection).
- Figure 9-1: remove LF pin connection.
- Figure 9-5: replace LF with NC (no connection)
- Figure 9-6: replace LF with NC (no connection)
- Section 9.7: delete section.

**2. Module: Reference Clock (REFCLK) Pin 7 changed to no connection**

REFCLK pin 7 has been changed to no connection (NC). All references in the data sheet referring to the REFCLK pin should be deleted.

- Pin Diagram page 1: replace REFCLK with NC (no connection).
- Figure 1-1: remove Clock Divider block and REFCLK pin connection.
- Table 1-1: replace REFCLK with NC (no connection).
- Figure 9-1: remove Clock Divider block and REFCLK pin connection.
- Figure 9-5: replace REFCLK with NC (no connection)
- Figure 9-6: replace REFCLK with NC (no connection)
- Section 9.6: delete section.

# rfPIC12F675K/675F/675H

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## APPENDIX A: REVISION HISTORY

Rev A Document (11/2003)

Original errata document.

Issue 1 – VDD and VDDRF Supply Voltage maximum has been reduced to 4.5V.

Issue 2 – The loop filter (LF) signal will no longer be bonded out. Pin 13 is a no connection.

Issue 3 – The reference clock (REFCLK) signal will no longer be bonded out. Pin 7 is a no connection

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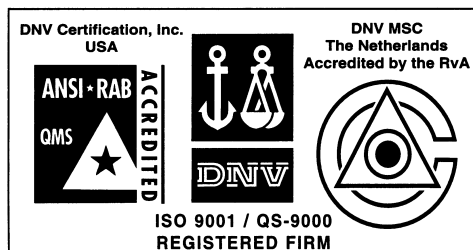
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